

2SC1360(A)

Rev.E Mar.-2016

/ Descriptions

TO-92LM NPN Silicon NPN transistor in a TO-92LM Plastic Package.

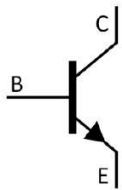
/ Features

High f_T , large P_C .

/ Applications

Picture IF amplifier.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

See Marking Instructions.

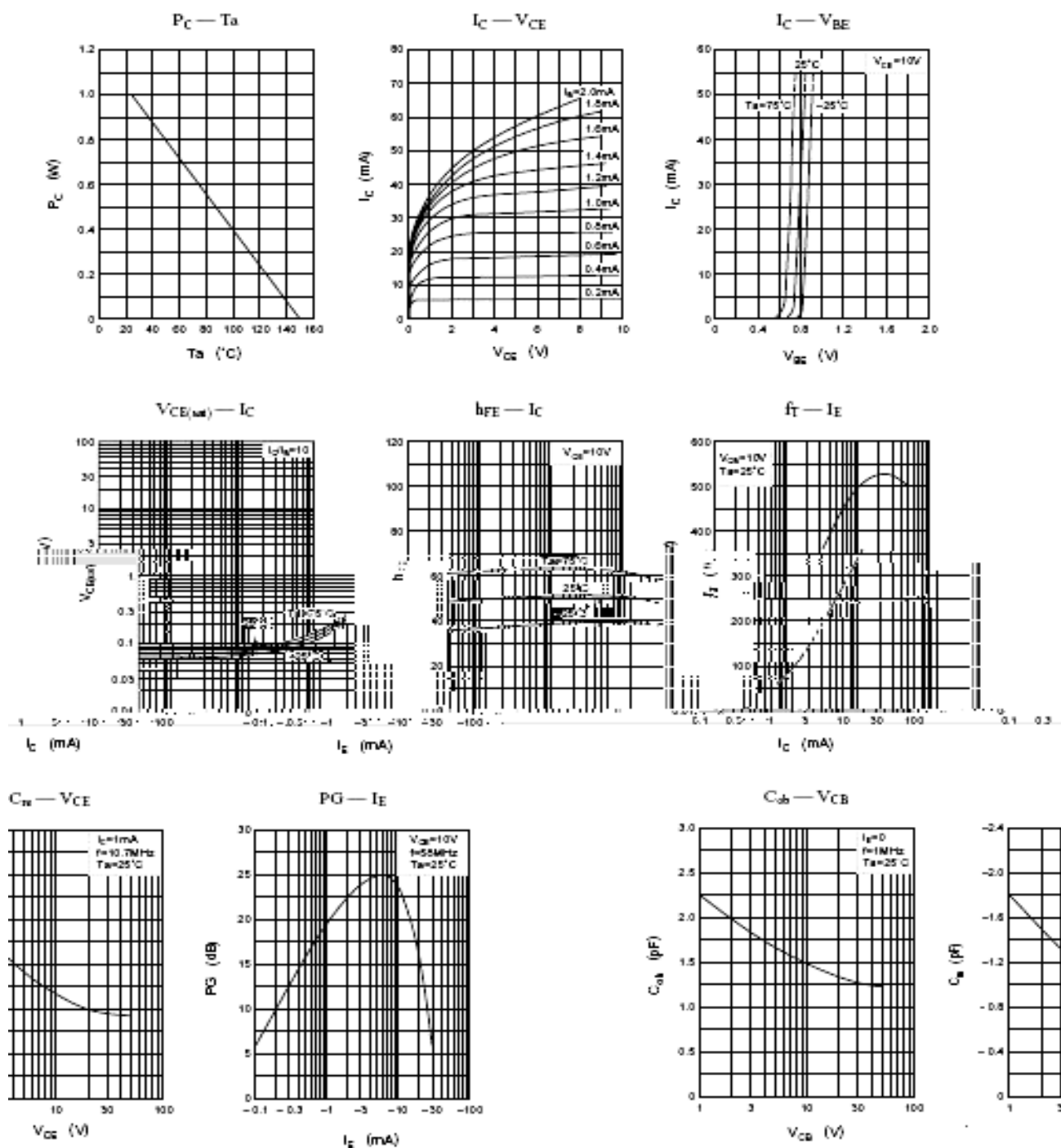
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Parameter	Symbol		Rating	Unit
Collector to Base Voltage	V_{CBO}	2SC1360	50	V
		2SC1360A	60	
Collector to Emitter Voltage	V_{CEO}	2SC1360	45	V
		2SC1360A	60	
Emitter to Base Voltage	V_{EBO}		4.0	V
Collector Current - Continuous	I_C		50	mA
Collector Power Dissipation	P_C		1.0	W
Junction Temperature	T_j		150	°C
Storage Temperature Range	T_{stg}		-55 150	°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Base Breakdown Voltage	V_{CBO}	2SC1360 $I_C = -10\text{ A}$ $I_E = 0$	50			

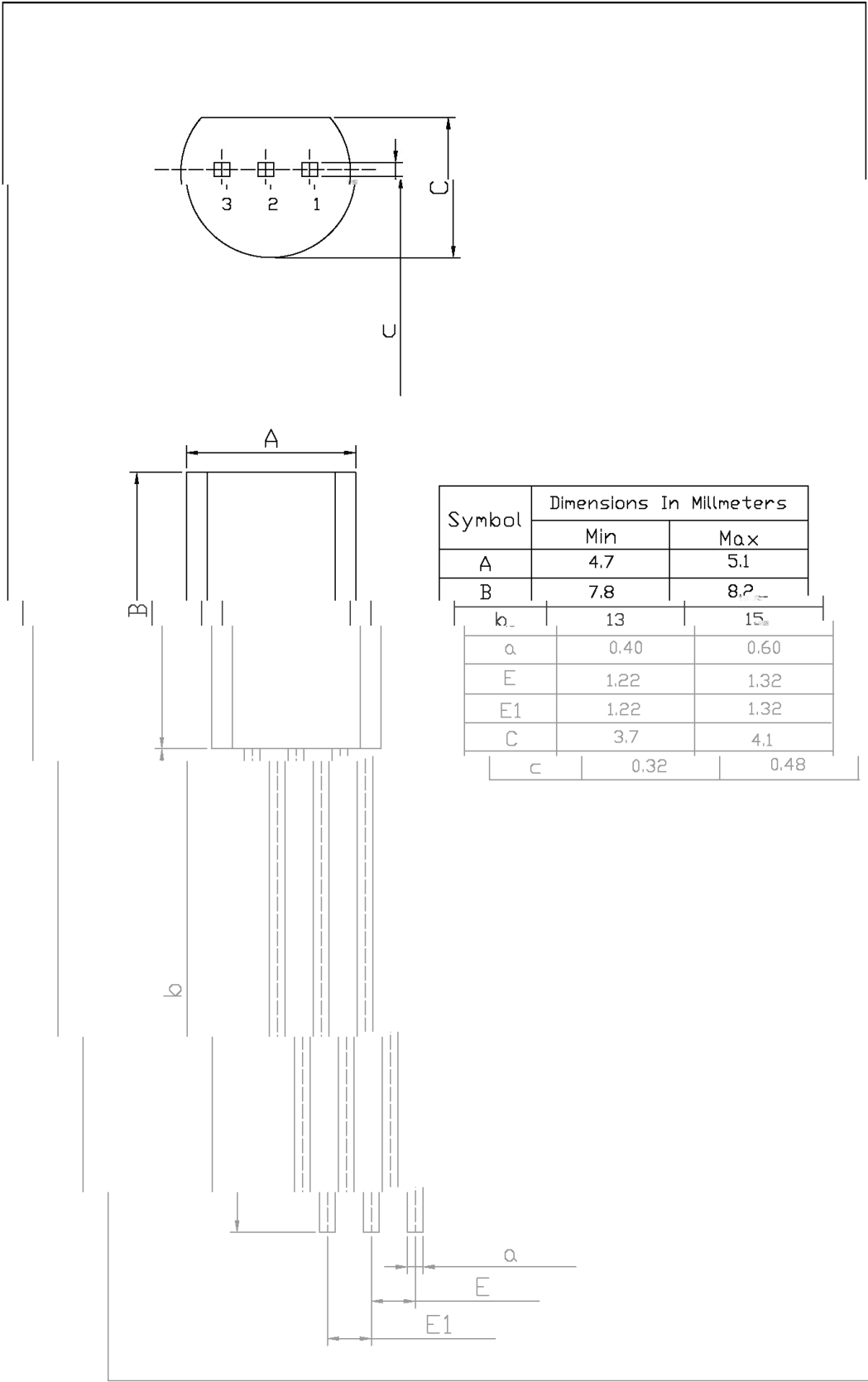
/ Electrical Characteristic Curve



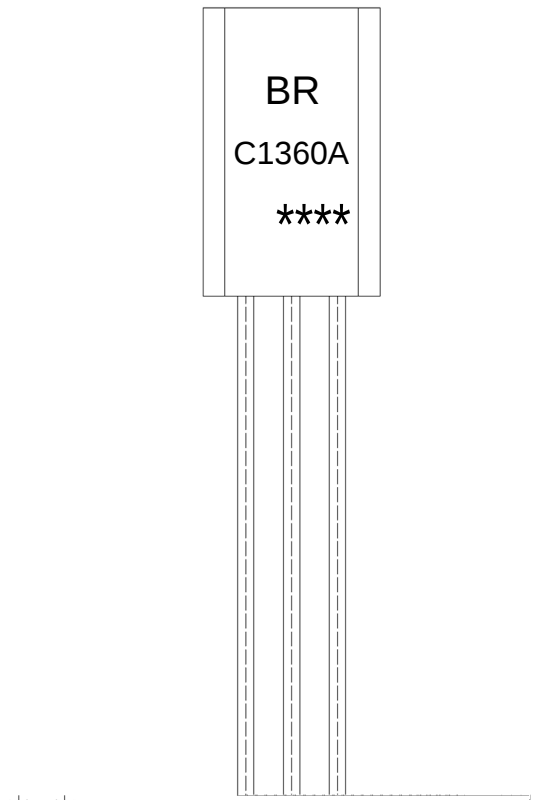
/ Package Dimensions

T0-92LM

Unit: mm



/ Marking Instructions



BR:

C1360A

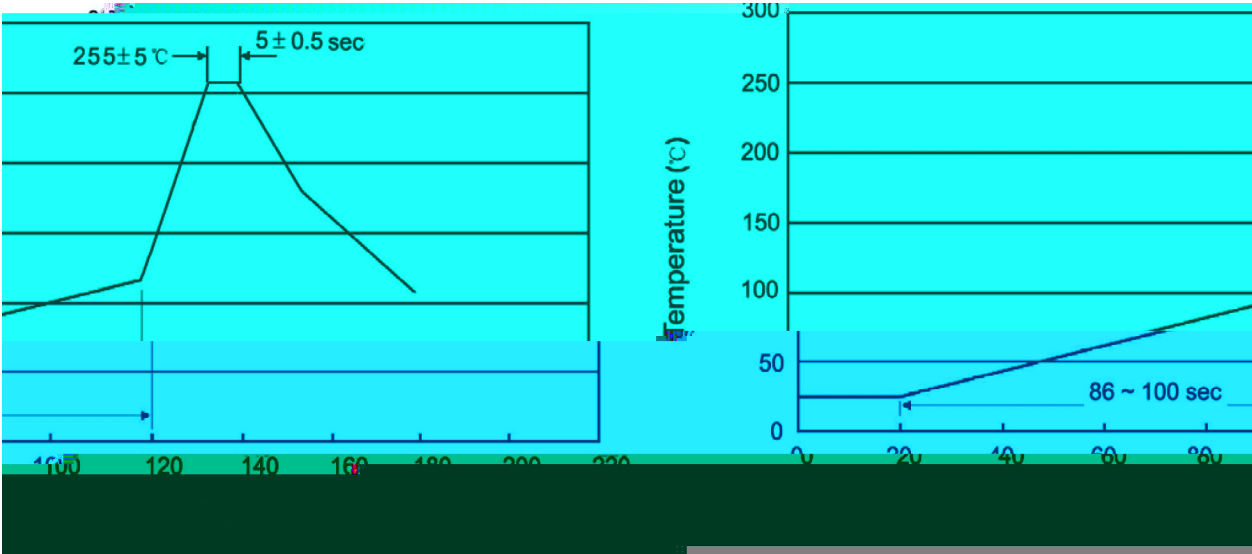
Note:

BR: Company Code.

C1360A: Product Type.

****: Lot No. Code,code change with Lot No.

() / Temperature Profile for Dip Soldering(Pb-Free)



Note:

- 1

25 150

60 90sec;
- 2

255±5

5±0.5sec;
- 3

2 10

/sec.
- 1.Preheating:25~150

, Time:60~90sec.
- 2.Peak Temp.:255±5

, Duration:5±0.5sec.
3. Cooling Speed: 2~10

/sec.

/ Resistance to Soldering Heat Test Conditions

270±5

10±1 sec.

Temp.:270±5

Time:10±1 sec

/ Packaging SPEC.

/ BULK

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)		
	Units/Bag 只/袋	Bags/Inner Box 袋/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Bag 袋	Inner Box 盒	Outer Box 箱
TO-92LM	1,000	8	8,000	5	40,000	135×190	237×172×102	560×245×195

/ AMMO

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)		
	Units/tape 只/纸带	Tape/Inner Box 纸带/盒	Rows/Inner Box 纸带层/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Inner Box 盒	Outer Box 箱	
TO-92LM	2,500	1	100	10	25,000	328×230×42	小箱 480×346×235 大箱 547×407×268	