

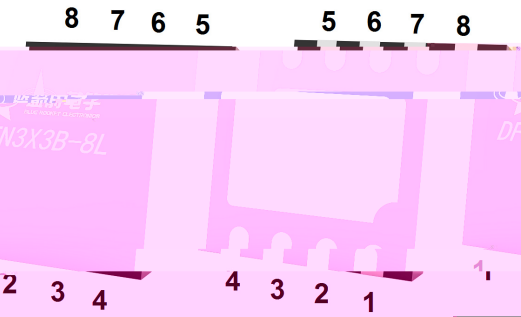
/ Features

- ## / Applications

- ## / Application Circuit



/ Pinning



PIN Num.	Symbol	Function
		U j r
	SUR J	U U U U U U U U
		U U
		U 8Y
		U U
		U U U U U U U U
		U U U U U U U U
		U U U U U U U U

/ Absolute Maximum Ratings(Ta=25℃)

PARAMETER	SYMBOL	RATINGS	UNITS

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Power Suplly						
						μ
						μ
						μ
Charging Current Setting						

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Indicator Pin Status CHRG/STDBY						

BRCL4058NZR-4.2



/ Function description



1. The device is a high-speed, high-precision, and high-reliability digital signal processor (DSP) with a 16-bit word length and a 16-bit data path. It is designed for use in a wide range of applications, including digital filtering, signal processing, and control systems. The device is available in a 16-pin DIP package and a 16-pin QFP package.



2. The device is a high-speed, high-precision, and high-reliability digital signal processor (DSP) with a 16-bit word length and a 16-bit data path. It is designed for use in a wide range of applications, including digital filtering, signal processing, and control systems. The device is available in a 16-pin DIP package and a 16-pin QFP package.



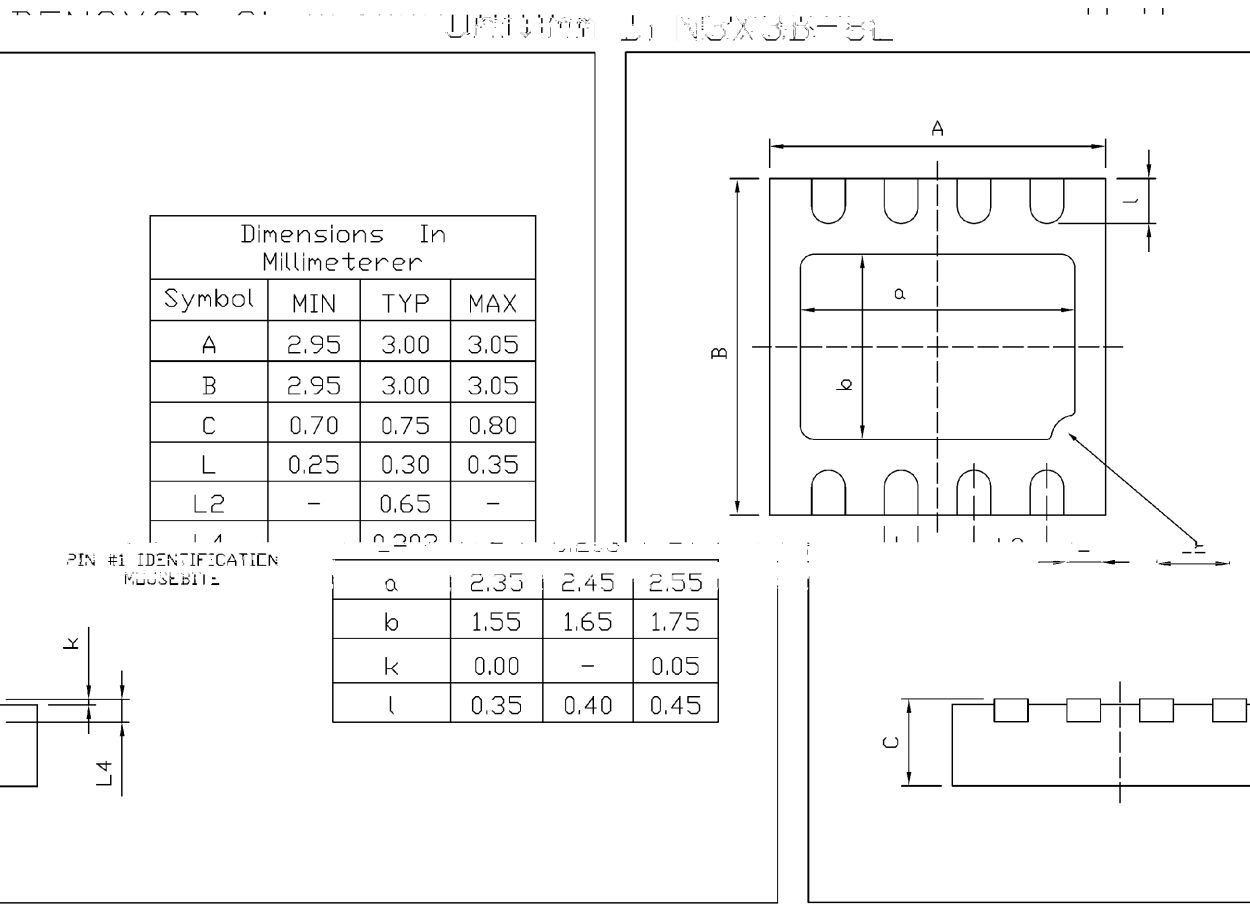
3. The device is a high-speed, high-precision, and high-reliability digital signal processor (DSP) with a 16-bit word length and a 16-bit data path. It is designed for use in a wide range of applications, including digital filtering, signal processing, and control systems. The device is available in a 16-pin DIP package and a 16-pin QFP package.



4. The device is a high-speed, high-precision, and high-reliability digital signal processor (DSP) with a 16-bit word length and a 16-bit data path. It is designed for use in a wide range of applications, including digital filtering, signal processing, and control systems. The device is available in a 16-pin DIP package and a 16-pin QFP package.

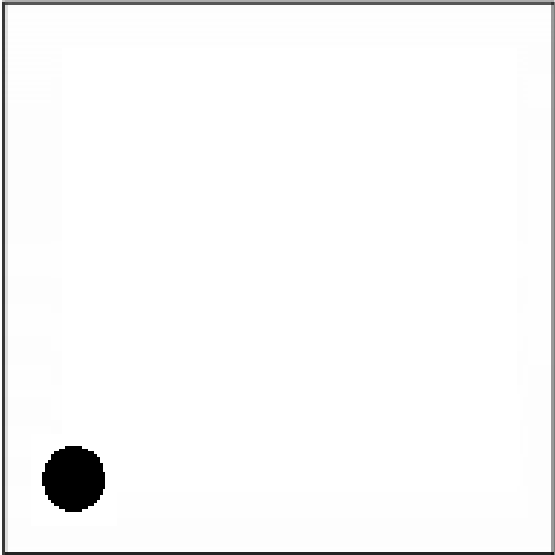
↑	~ σ	~ σ
* ɔ		ð
ɔ ɒ	ð	
ɔ H	ð	ð
ɔ +WHPS ɪ ,		
YGG j	♣	
ɔ		

/ Package Dimensions



Rev.00 202006

/ Marking Instructions



EU Ⅱ .
738;Q N .
XX 3 ī ~
---- 1 N . 1 N ψ

N	γ

