

/ Descriptions			
BRCM24C08SC	SOP-8	8 Kbit	I ² C

/ Features			
1.7V	2.5~5.5V	1Mhz	1.7~2.5V
400Khz			
CMOS	400uA	1.6mA	
16			
128			
5ms			
100	100	ESD HBM	6KV
+/-200mA;			

/ Applications

/ Pinning



/ Pinning

Pin	Name	Type	Description
1	NC	-	
2	NC	-	
3	E2	Input	
4	GND	Ground	
5	SDA	I/O	/
6	SCL	Input	
7	WCB	Input	
8	VCC	Power	

/ Marking

/ Absolute Maximum Ratings(Ta=25)

			mA
			V

/ Reliability Characteristic

		Ñ				

		○				
		○				

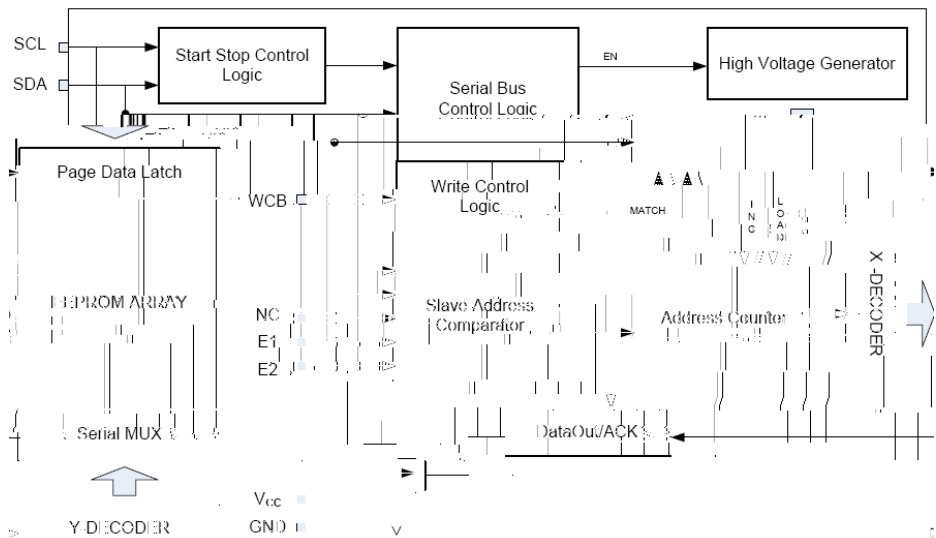
		≤			≤ ≤			

/ AC Electrical Characteristics(Unless otherwise specified, Vcc = 1.7V to 5.5V,

T_A = 40

		≤			≤ ≤			

≤



/ Functional Description

H / Data Input

SDA SDA SCL (1) SCL
(1)

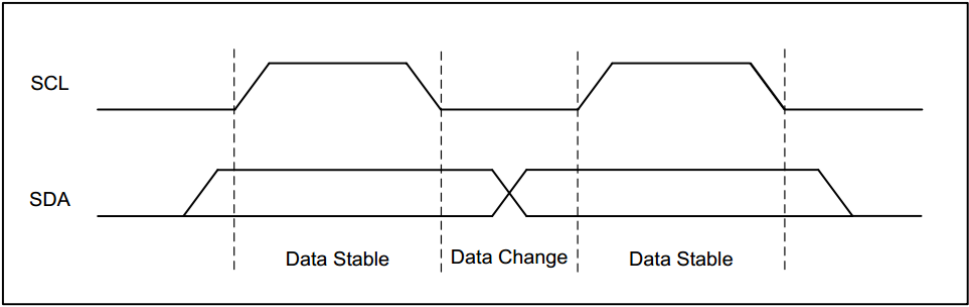


Figure 1 Data Validity

H / Start Condition

/ Functional Description

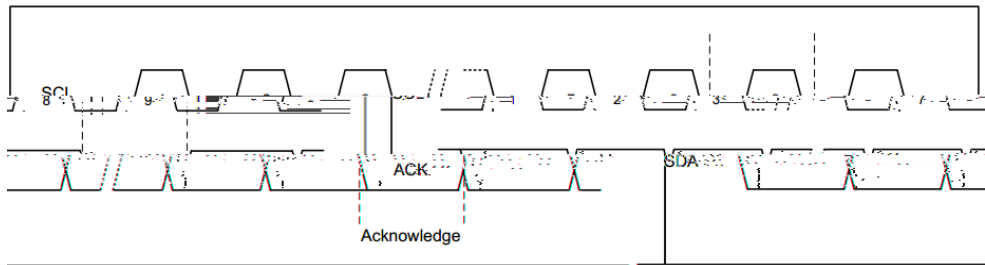


Figure 3 Output Acknowledge

H / Standby Mode

BRCM24C08SC : (a) , (b)
, (c)

H / Soft Reset

, (c) (a) , (b)
4

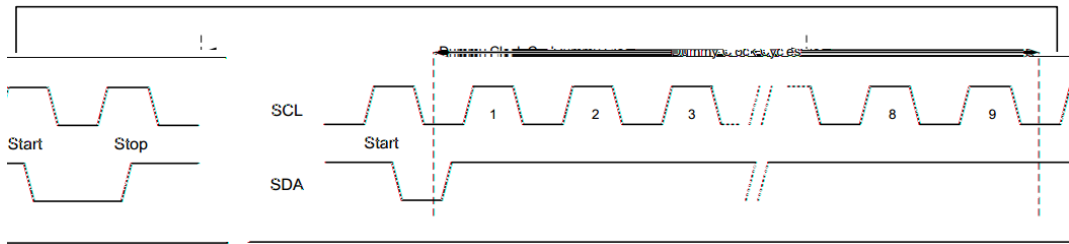


Figure 4 Soft Reset

H / Device Addressing

BRCM24C08SC 8 1
1 0

/ Functional Description

E228 / E20

10

H / Data Security

BRCM24C08SC WCB Vcc

H / Byte Write

8

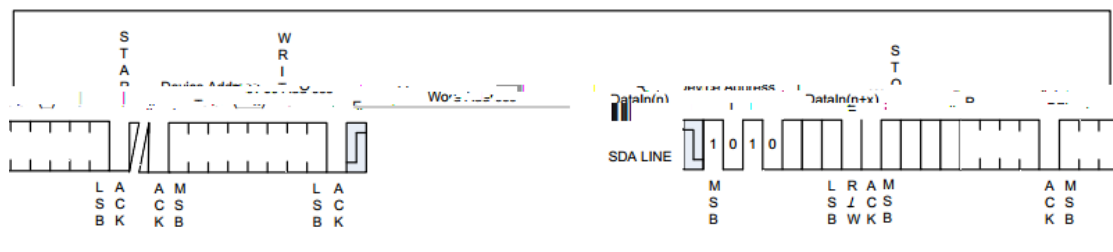
BRCM24C08SC “ 0 ” 8 8

BRCM24C08SC “ 0 ”

BRCM24C08SC BRCM24C08SC

(5)

Timing diagram for the SPI interface. The diagram shows the relationship between the clock signal (SCK), the data bus (Data In, Data Out), and the address bus (Device Address, Word Address). The Data In signal is shown as a sequence of bits: 1, 0, 1, 0, followed by a black box indicating a continuation of the data. The Data Out signal is shown as a sequence of bits: M, S, B, L, R, A, M, S, /, C, S, B, W, K, B, A, M, C, S, K, B.



/ Functional Description

H / Acknowledge Polling

BRCM24C08SC

/ BRCM24C08SC

“ 0 ”

H / Write Identification Page

16

(a) 1011b

(b) A5 / A4 0 1 A7 / A6 “ 00 ”

(c) A3 / A0

NoACK

H / Lock Identification Page

ID Lock ID

(a) 1011b;

(b) A6 1 ;

(c) xxxx xx1x x 0 1

/ Functional Description

H / Read Operations

/ “ 1 ”

;

H

/ Functional Description

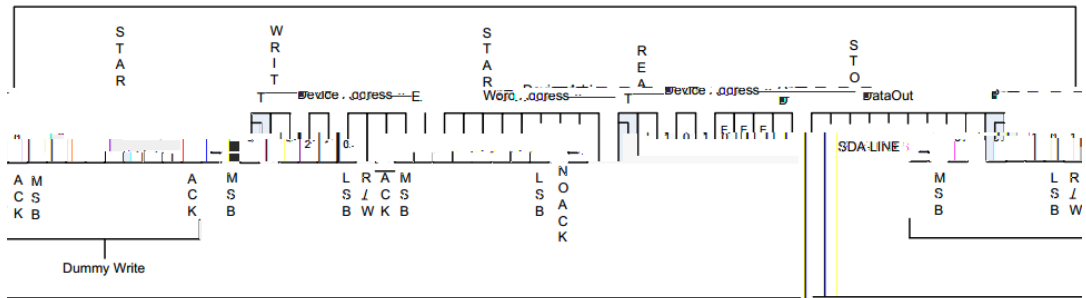


Figure 8 Random Address Read

H / Sequential Read

BRCM24C08SC

“ 0 ”

9

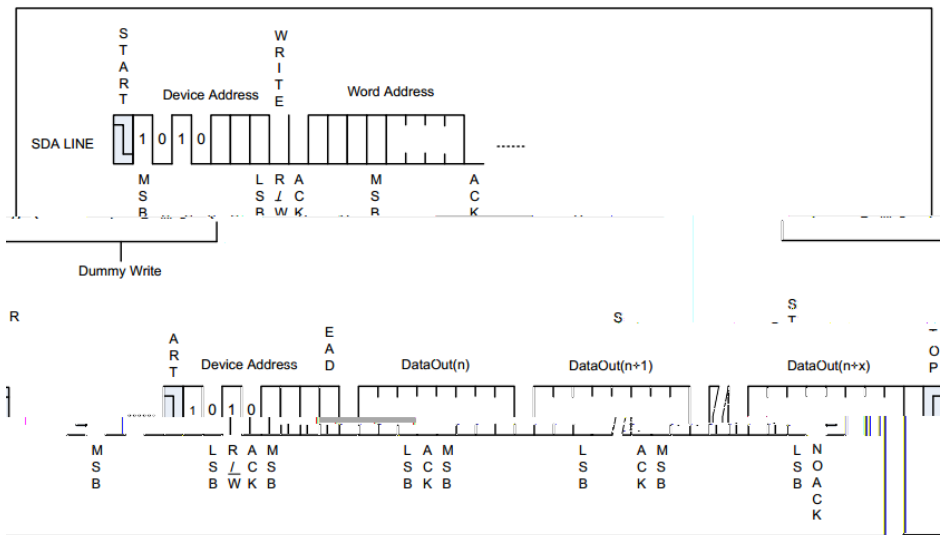


Figure 9

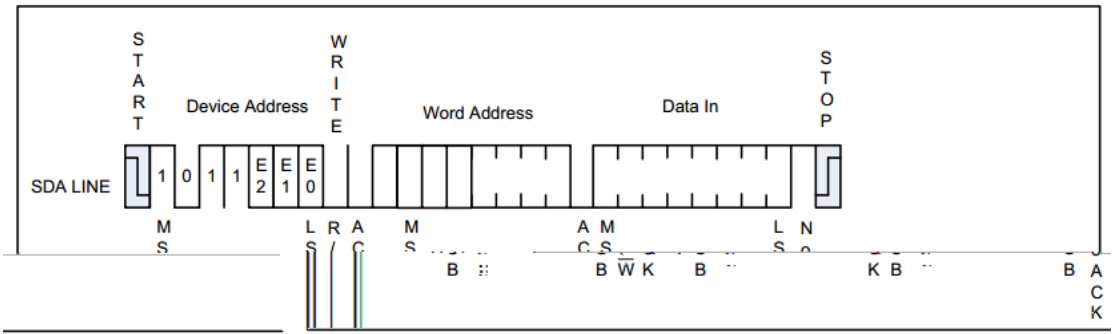
/ Functional Description

H / Read Identification Page

16									
1011b	A7 / A6	0	A5		A4 / A0		ID		
				10d			6	ID	
16									

H / Read the Lock Status

[ACK +] / NoACK,(10)



H / Read Serial Number

16									
				1					
				128					
			EEPROM				" "		
						EEPROM			
					1				

/ Functional Description

	A7	A6	“ 10 ”		2	“ 10 ”	
	80h						
128	16						
	128		ACK				
	7						

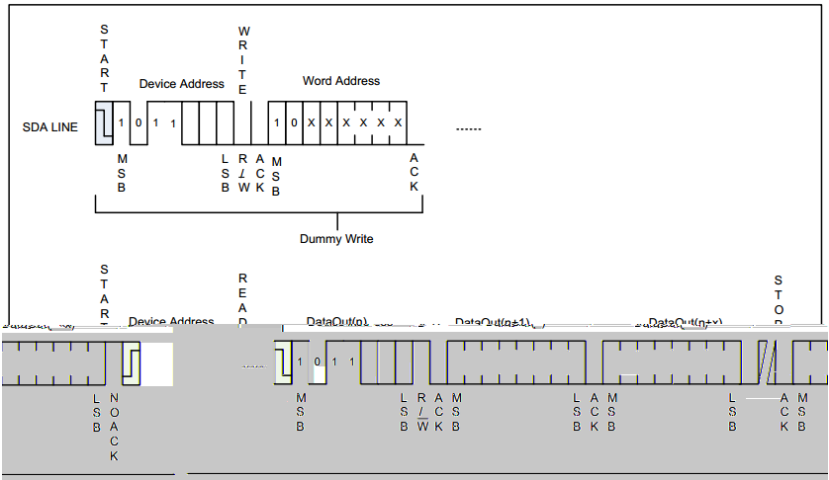
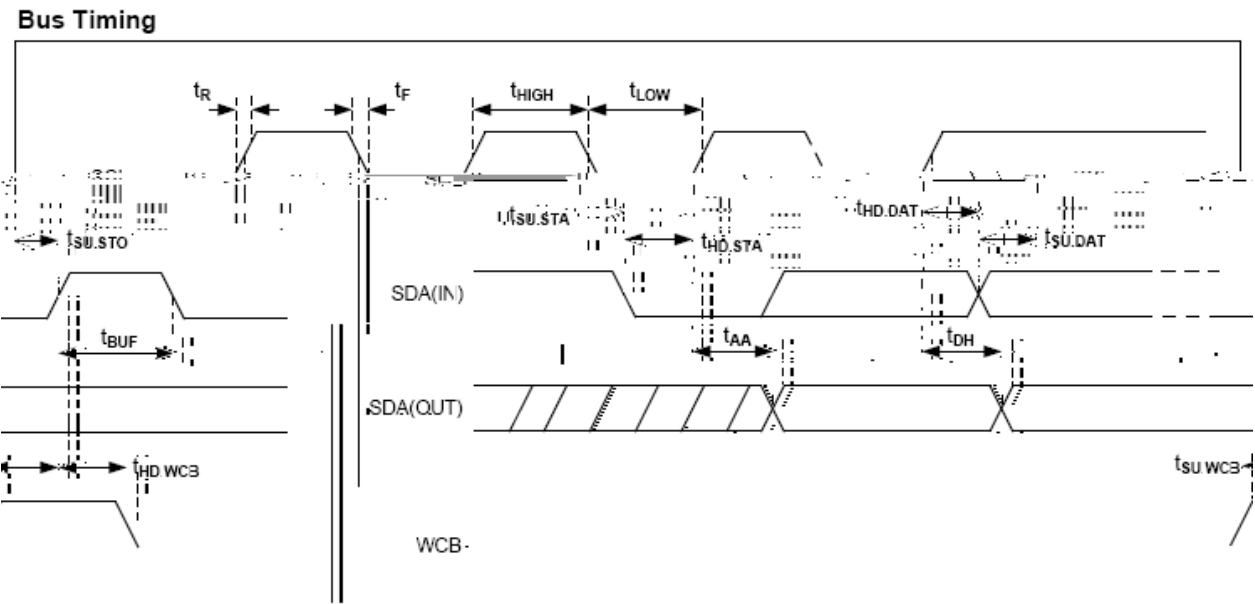
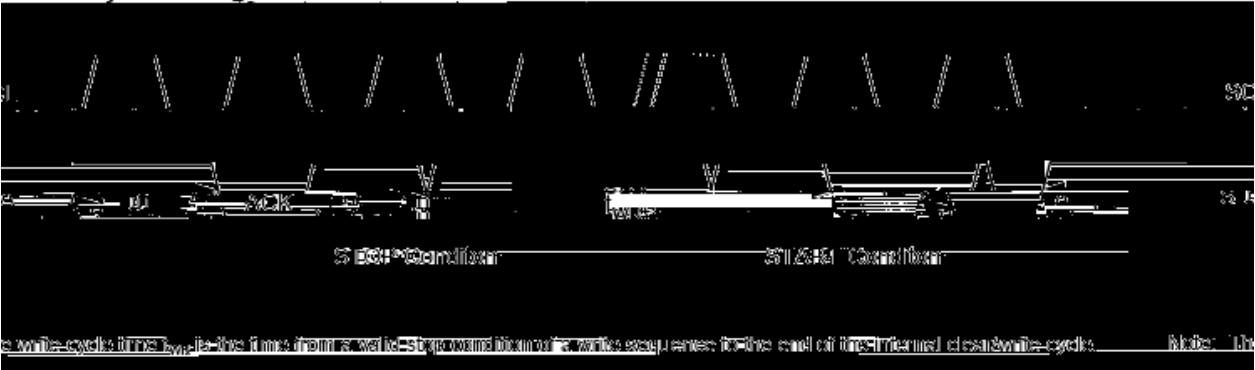


Figure 11 Sequential Read

/ Time Sequence Diagram



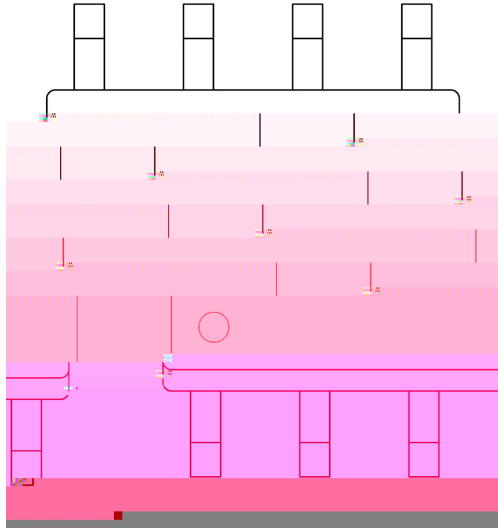
Write Cycle Timing



/ Package Dimensions



/ Marking Instructions



24C08

Figure 1 is a line graph showing the temperature profile of the test specimen. The y-axis represents Temperature (°C) from 0 to 350, and the x-axis represents Time (sec) from 0 to 280. The profile starts at 50°C, rises to 150°C at 60 ~ 90 sec, then to 245 ± 5°C at 180 ~ 200 sec, and finally cools down. A dwell time of 5 ± 0.5 sec is indicated at the peak temperature.

$$\frac{+}{-} \qquad \qquad \qquad \frac{+}{-} \qquad \qquad \qquad \frac{+}{-} \qquad \qquad \qquad \frac{+}{-}$$
$$\frac{+}{-} \qquad \frac{+}{-} \qquad \frac{+}{-} \qquad \frac{+}{-}$$

封装形式	包装数量					包装尺寸		
	只 卷盘	卷盘 盒	只 盒	盒 箱	只 箱		盒	箱

17 17