

/ Descriptions

PDFN3×3A-8L N MOS

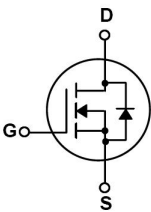
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/ Features

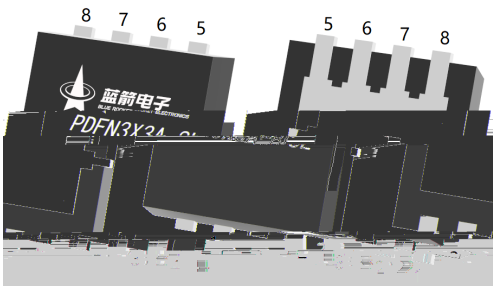
AEC-Q101

/ Applications

/ Equivalent Circuit



/ Pinning



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D

/ Marking

/ Absolute Maximum Ratings(Ta=25)

	≤			

/ Electrical Characteristics(Ta=25)

/ Electrical Characteristic Curve

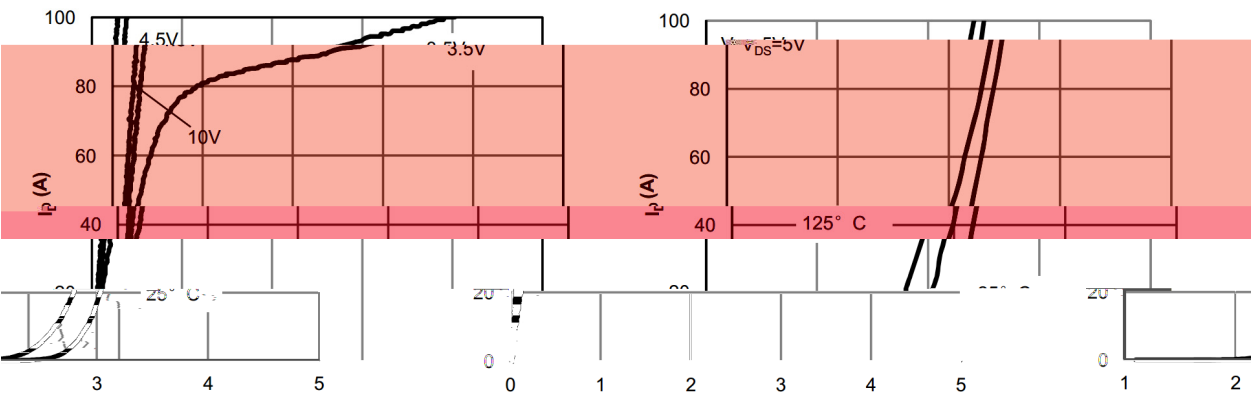


Figure 2: Transfer Characteristics

Figure 1: On-Region Characteristics

Figure

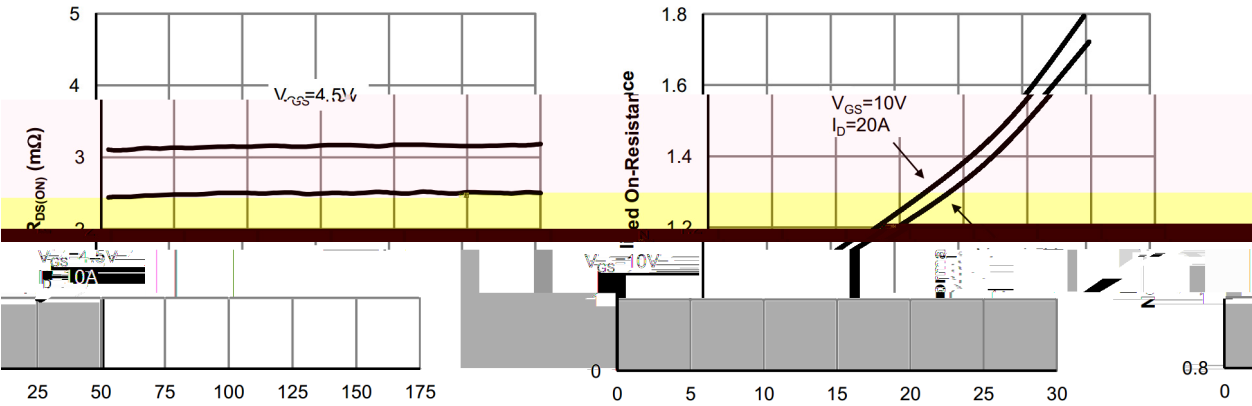


Figure 4: On-Resistance vs. Junction Temperature

Figure 3: On-Resistance vs. Drain Current and Gate Voltage

Fig

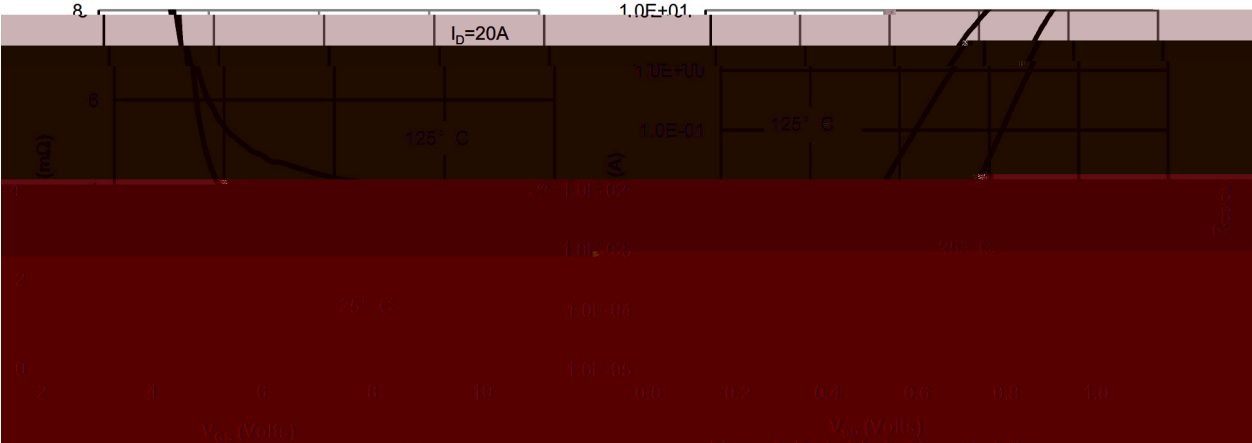


Figure 5: On-Resistance vs. Gate-Source Voltage

Figure 6: Body Diode Characteristics

/ Electrical Characteristic Curve

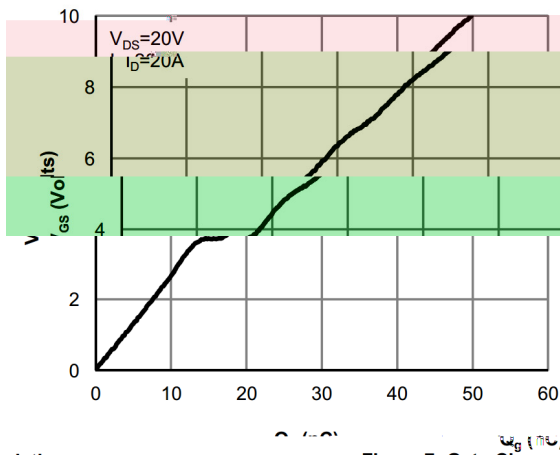


Figure 7: Gate-Charge Characteristics

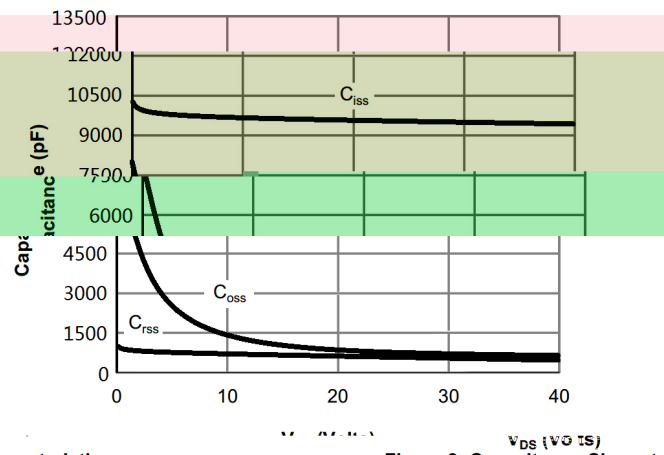
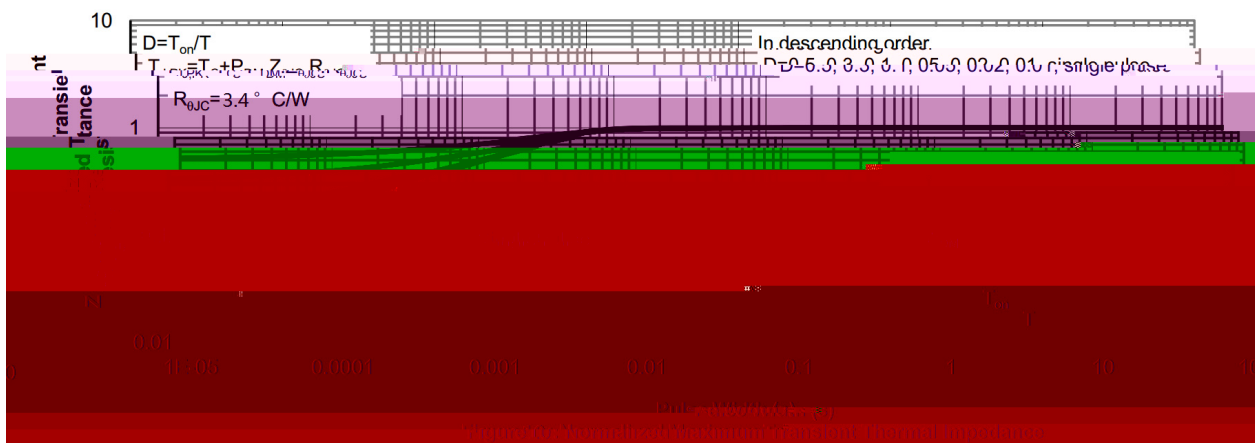
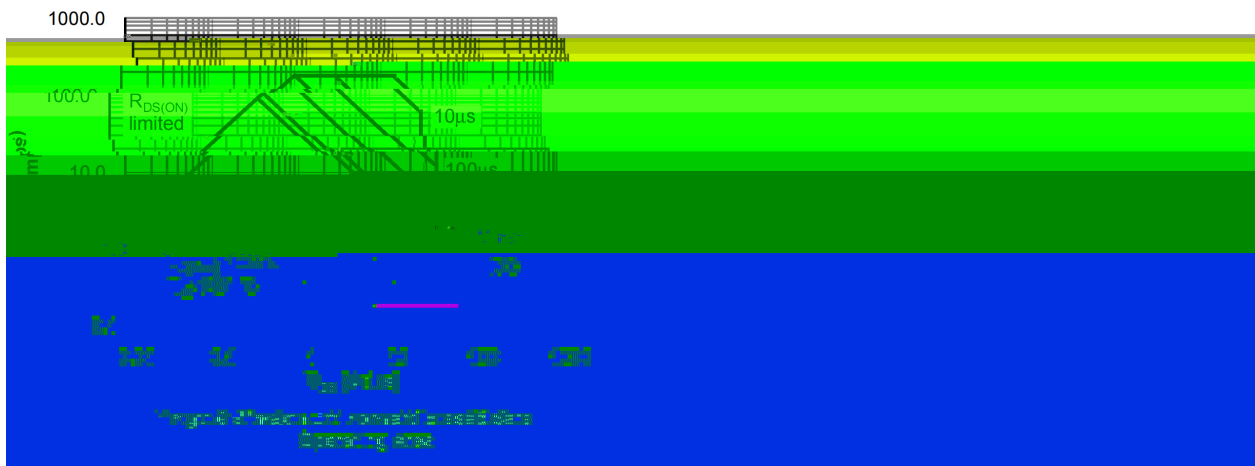


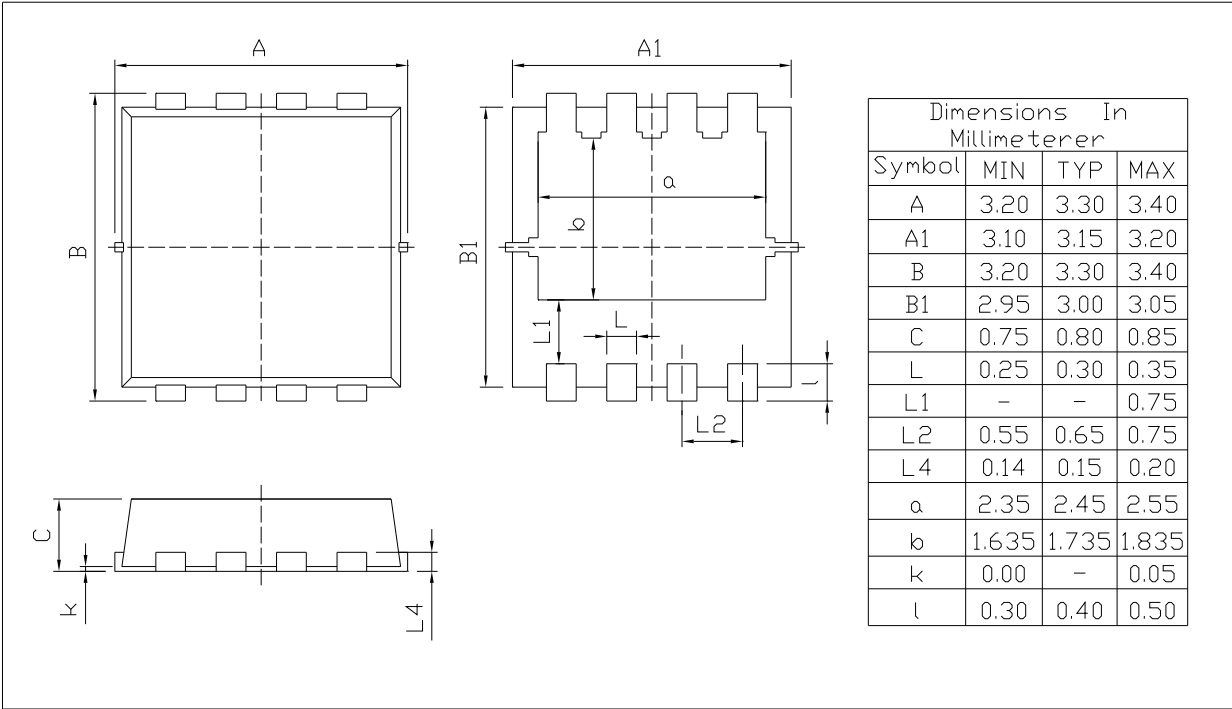
Figure 8: Capacitance Character



/ Package Dimensions

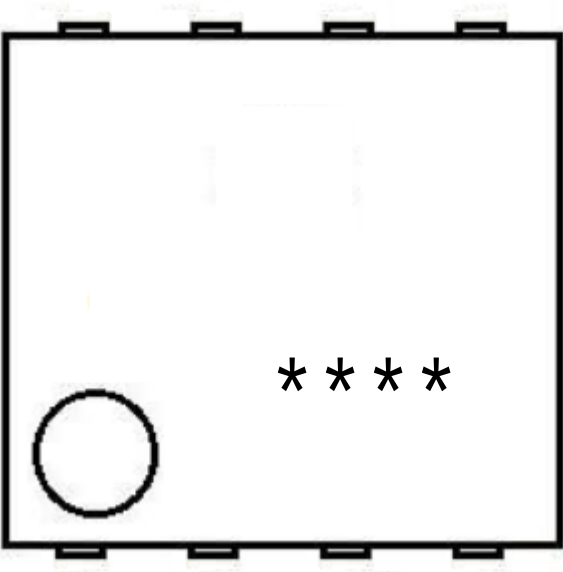
PDFN3X3A-8L

Unit:mm



Rev.00 202011

/ Marking Instructions



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