/ Descriptions

PDFN 3×3A-8L P MOS

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/ Features

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AEC-Q101 Â

/ Applications

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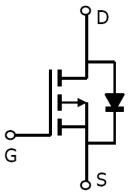
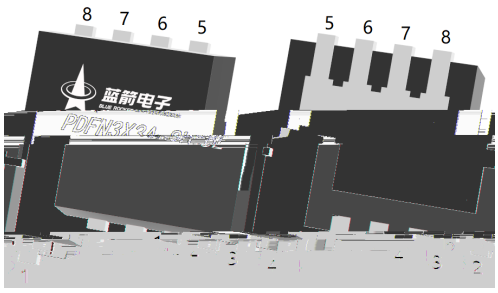
/ Equivalent Circuit



/ Pinning



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D

/ Marking

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/ Electrical Characteristics(Ta=25)

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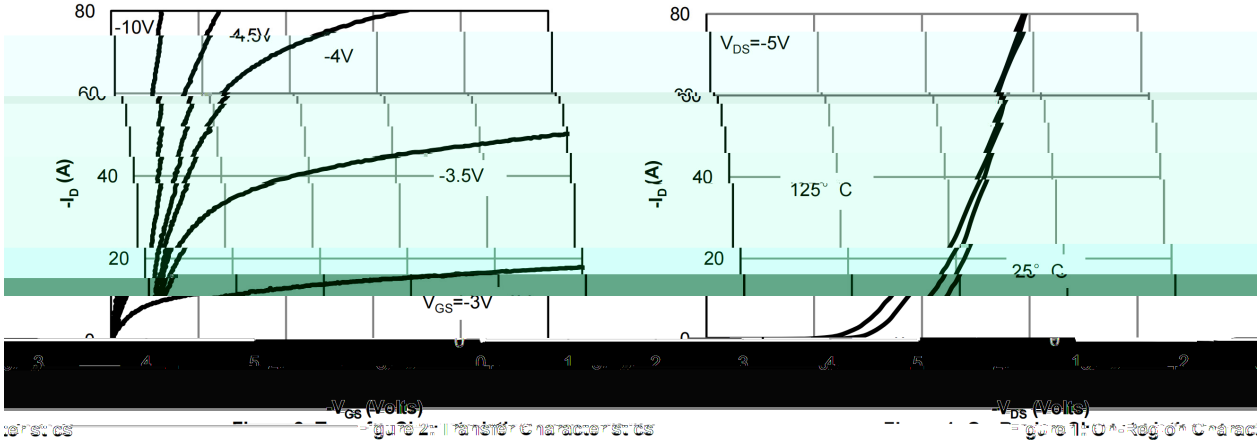
/ Electrical Characteristic Curve



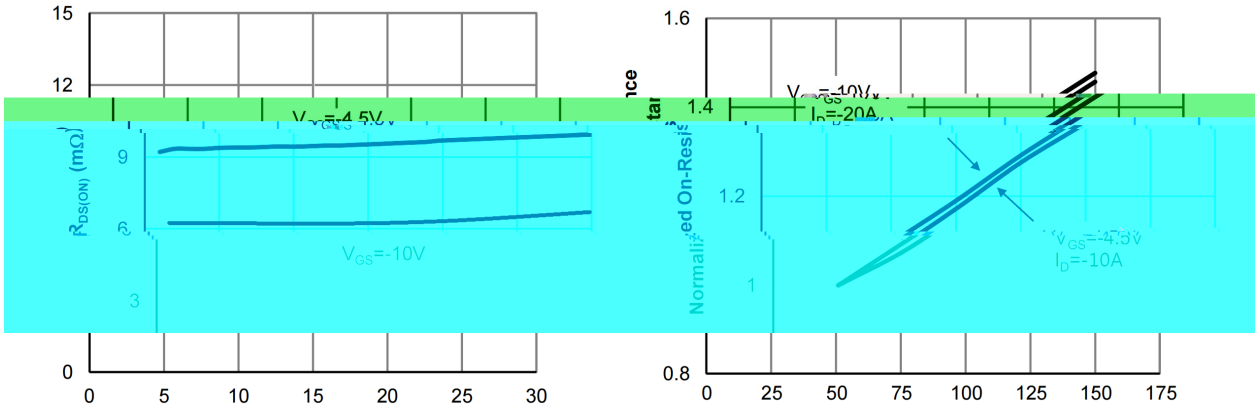


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

Figure 4: On-Resistance vs. Junction Temperature

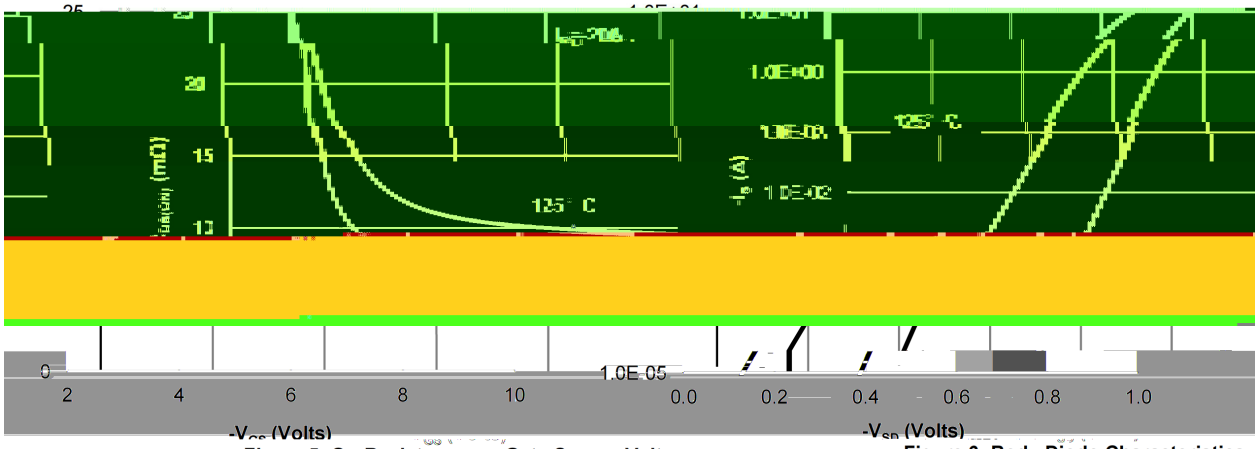
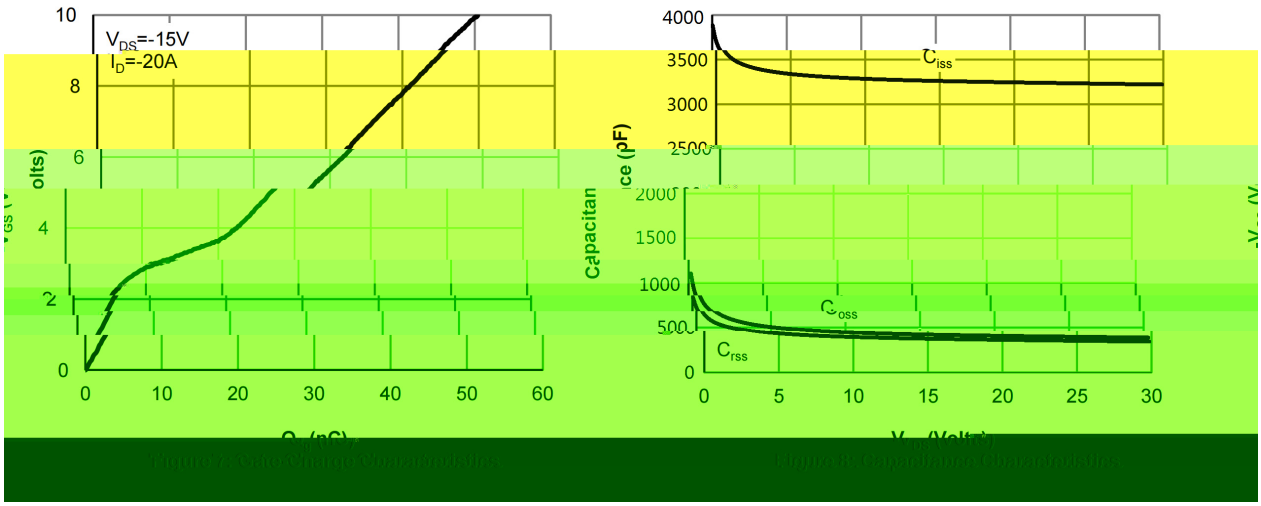
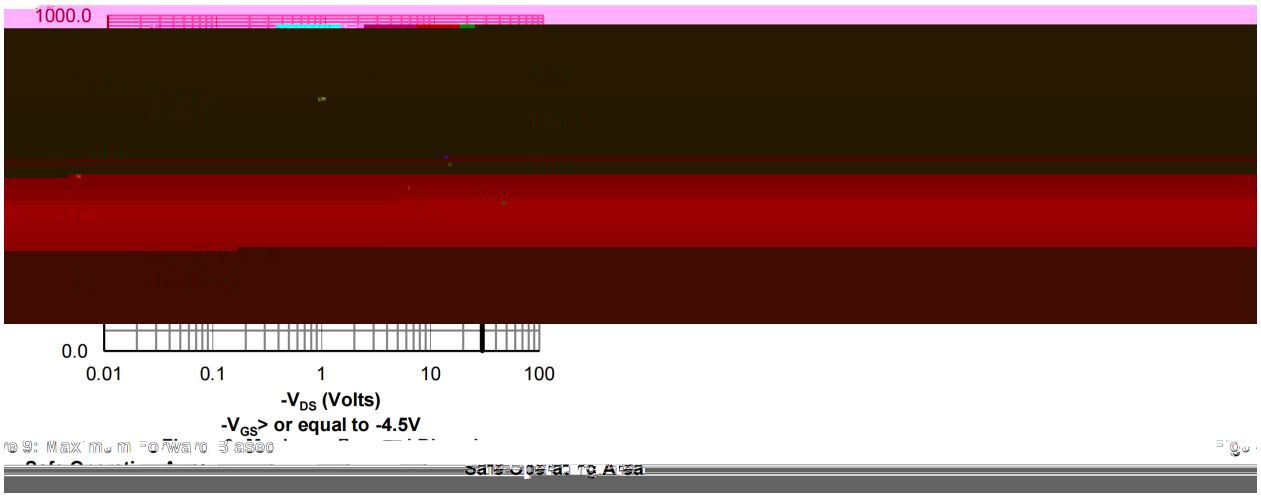


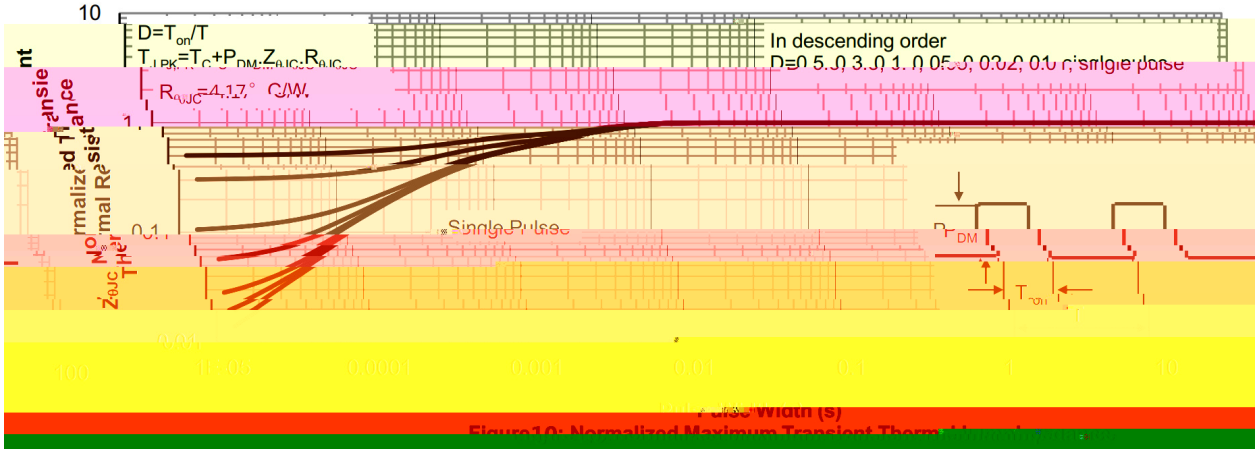
Figure 5: On-Resistance vs. Gate-Source Voltage

Figure 6: Body-Diode Characteristics

/ Electrical Characteristic Curve



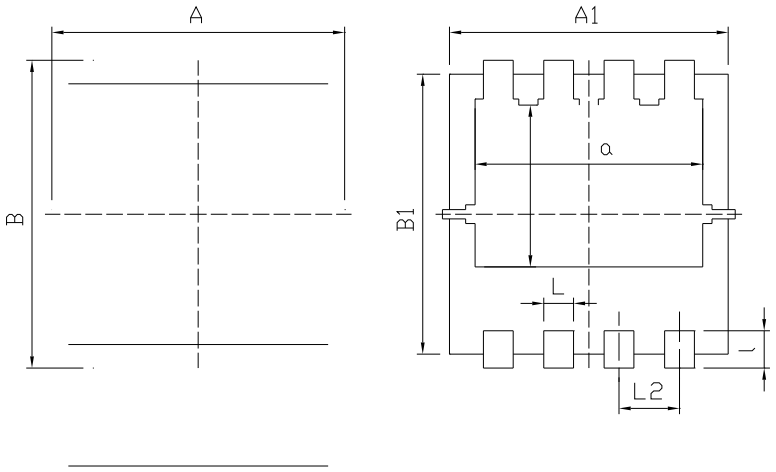




/ Package Dimensions

PDFN3X3A-8L

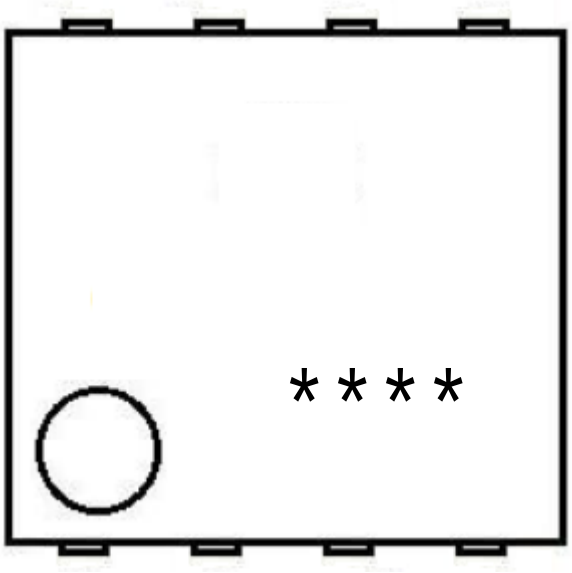
Unit:mm



Dimensions In Millimeterer			
Symbol	MIN	TYP	MAX
A	3.20	3.30	3.40
A1	3.10	3.15	3.20
B	3.20	3.30	3.40
B1	2.95	3.00	3.05
C	0.75	0.80	0.85
L	0.25	0.30	0.35
L1	-	-	0.75
L2	0.55	0.65	0.75
L4	0.14	0.15	0.20
a	2.35	2.45	2.55
b	1.635	1.735	1.835

Rev.00 202011

/ Marking Instructions



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( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)

± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ± ± ±

/ Packaging SPEC.

	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box	Outer Box