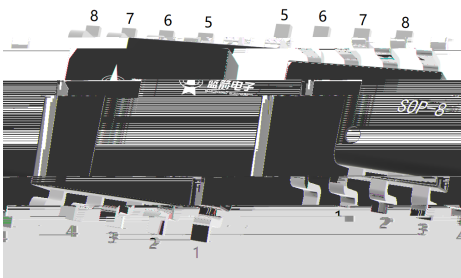
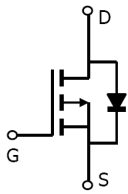


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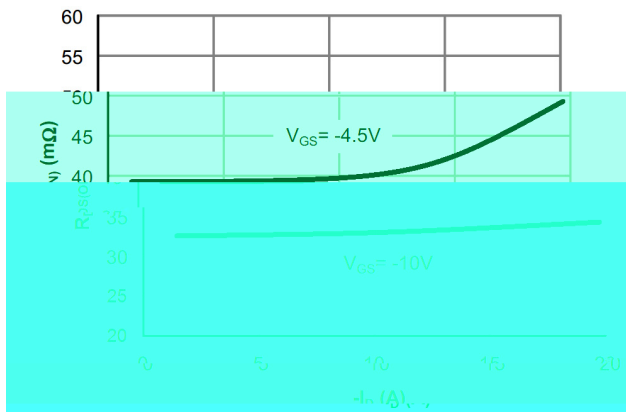
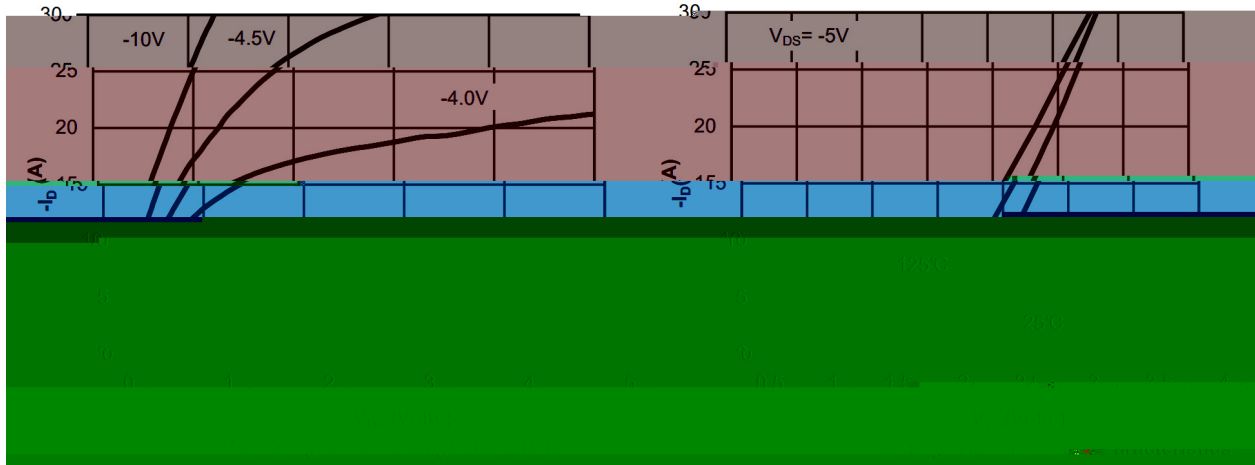


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

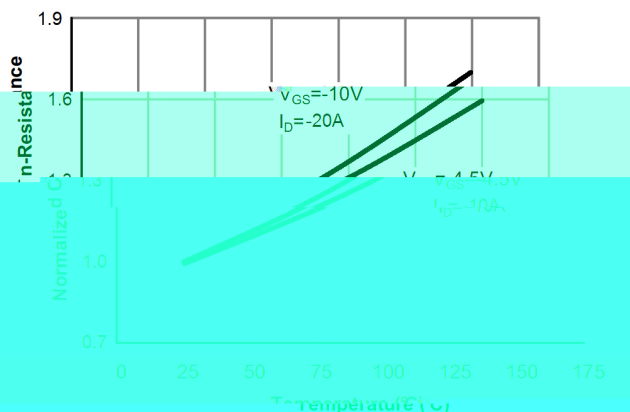


Figure 4: On-Resistance vs. Temperature

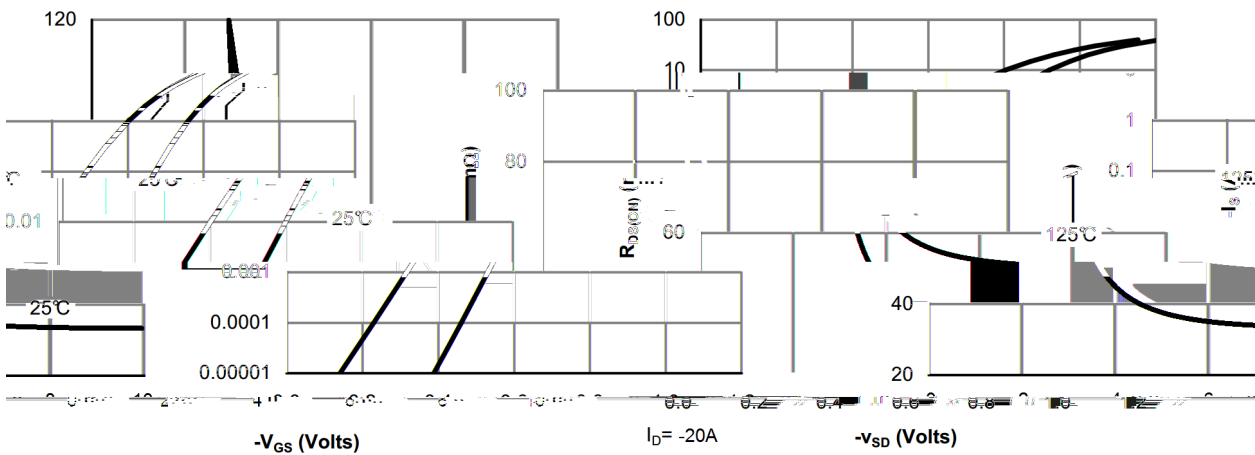


Figure 5: On-Resistance vs. Gate-Source Voltage

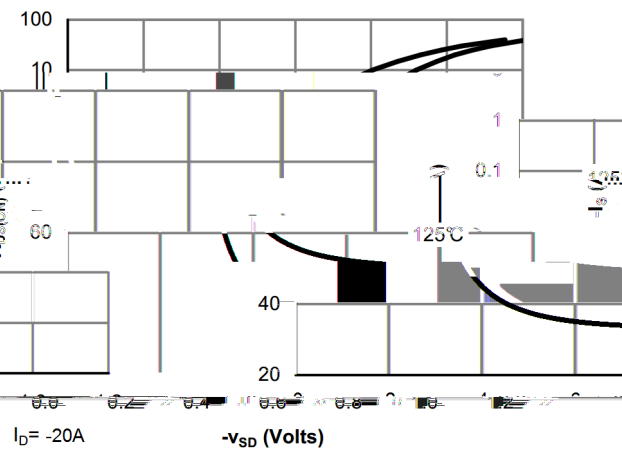


Figure 6: Body Diode Characteristics

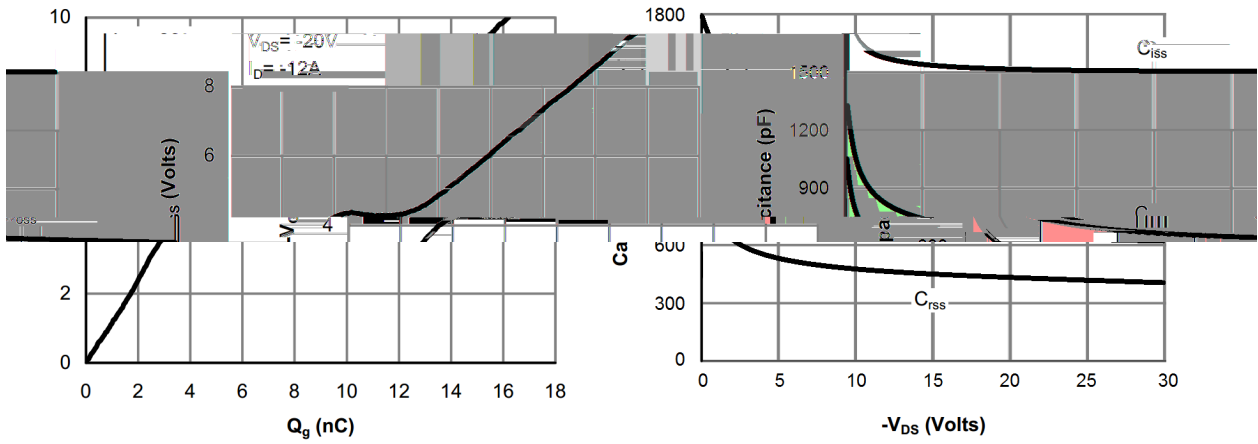


Figure 7: Gate-Charge Characteristics

Figure 8: Capacitance Characteristics

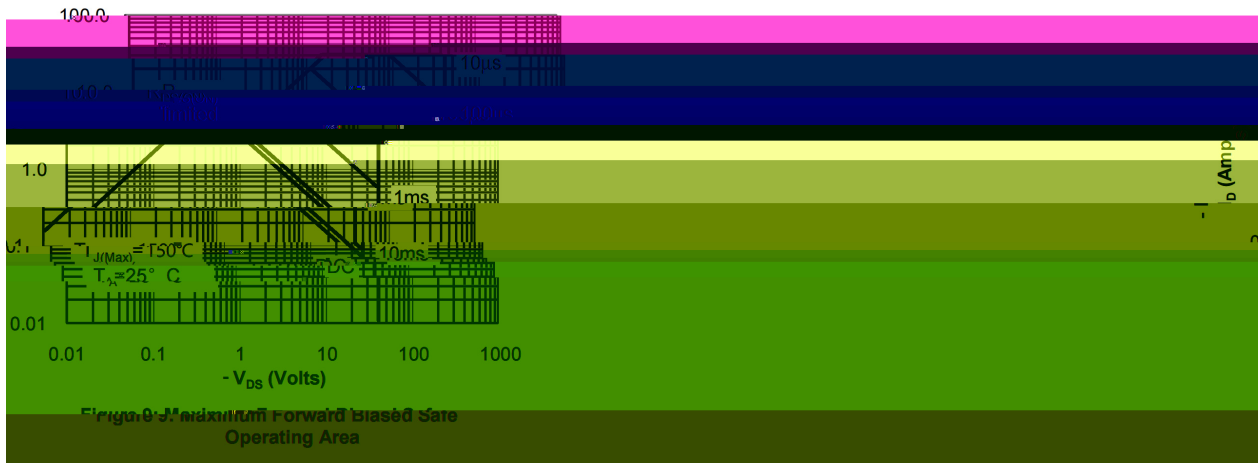


Figure 9: Maximum Forward Biased Safe Operating Area

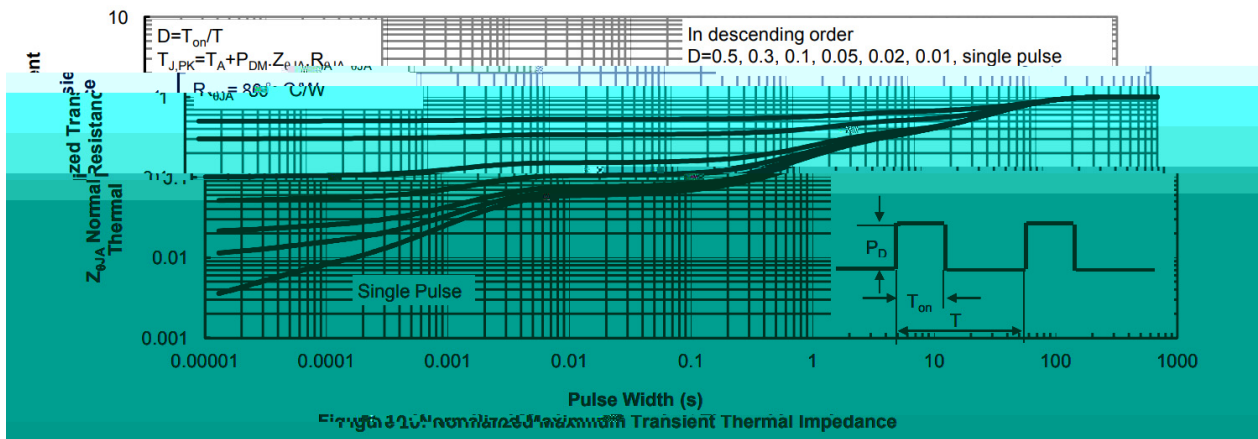
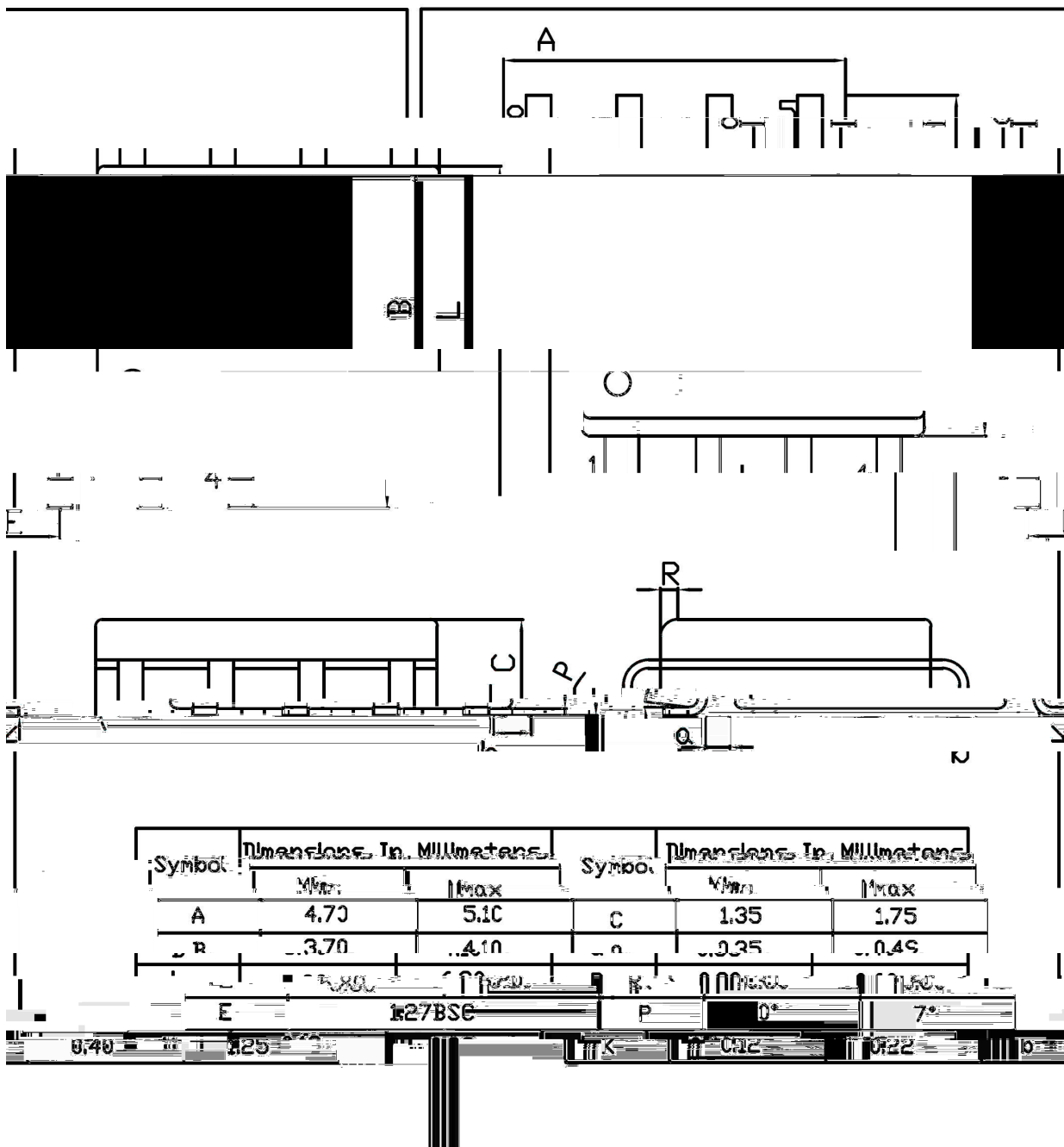
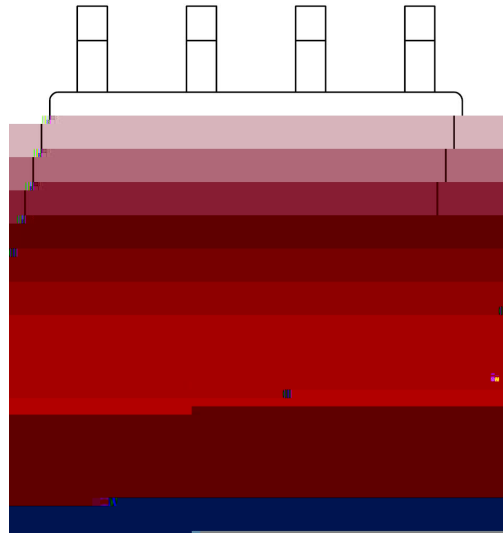
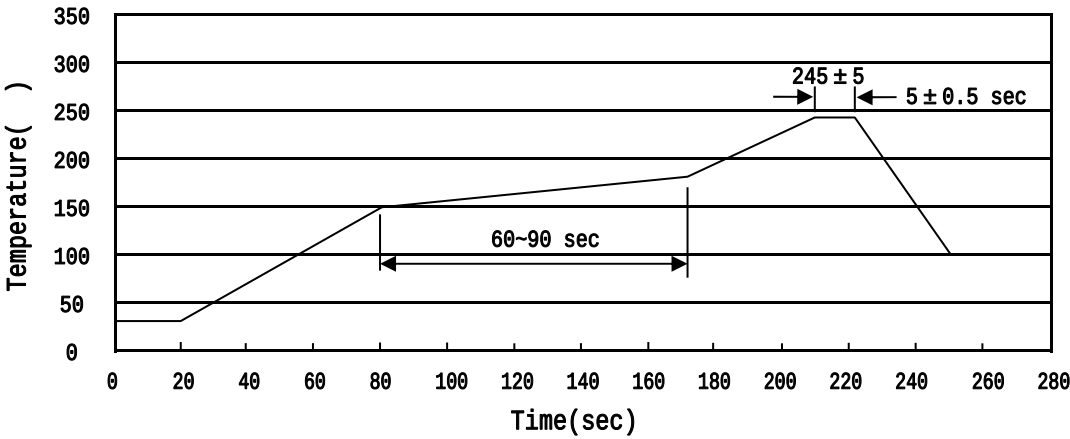


Figure 10: Normalized Maximum Transient Thermal Impedance

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