

/ Descriptions

SOT-89 PNP Silicon PNP transistor in a SOT-89 Plastic Package.

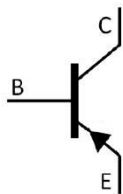
• / Features

 , KTC4376
Small flat package, complementary to KTC4376.

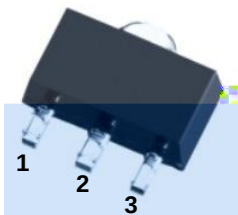
v / Applications

High current application.

≈ / Equivalent Circuit



λ / Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

. ↱ / Marking

h_{FE} Classifications Symbol	O	Y
h_{FE} Range	100 200	160 320
Marking	HRO [*]	HRY [*]

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-35	V
Collector to Emitter Voltage	V_{CEO}	-30	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-800	mA
Collector Base – Continuous	I_B	-160	mA
Collector Power Dissipation	P_C	500	mW
Collector Power Dissipation*	* P_C	1.0	W
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

*:mounted on ceramic substrate(250mm²×0.8t)

* 250mm²×0.8t

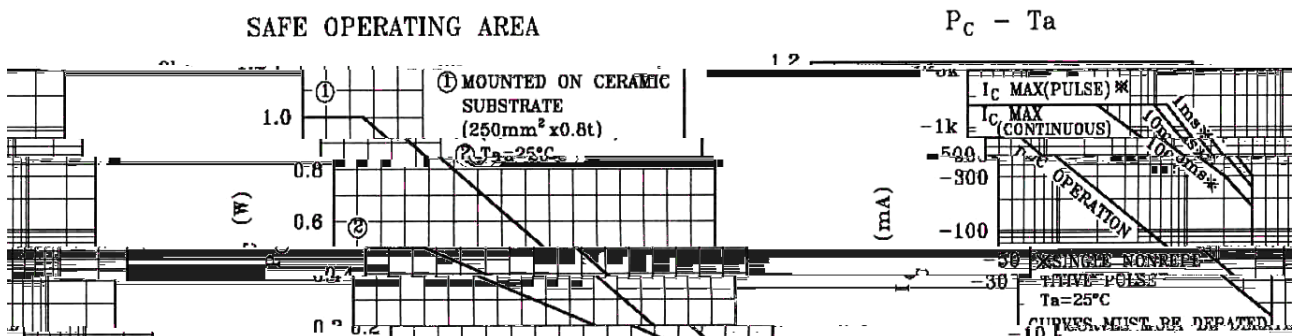
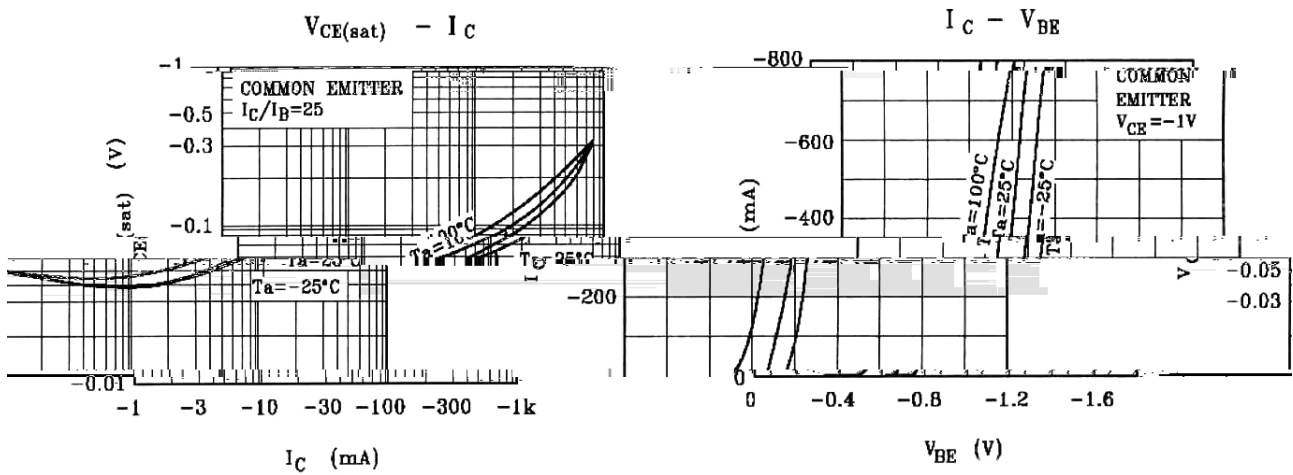
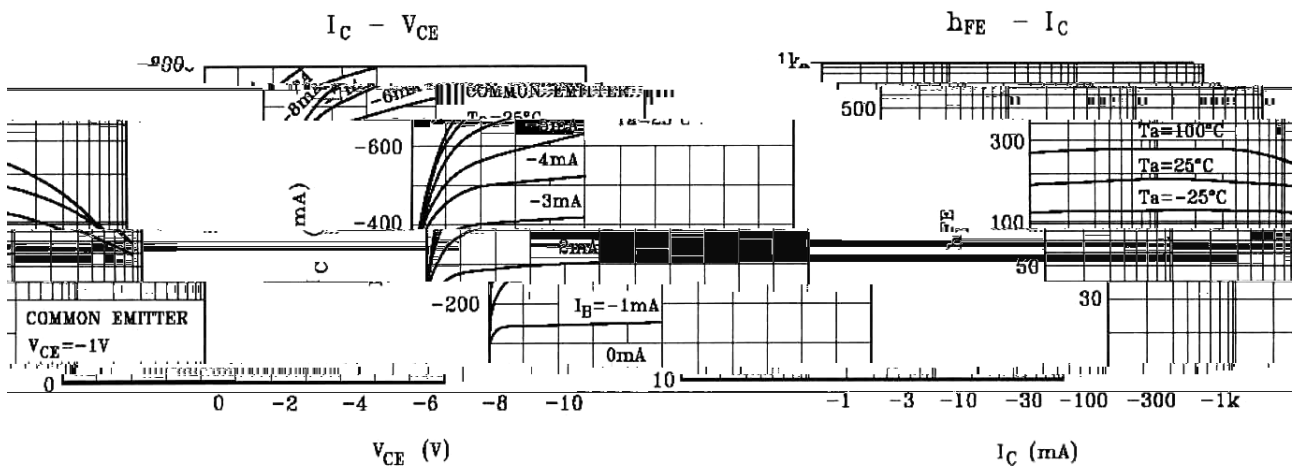


		Conditions	Min	Typ	Max	Unit
Collector to Emitter Breakdown Voltage	V_{CEO}	$I_C=-10mA$ $I_B=0$	-30			V
						μA

Emitter Base Cut-Off Current	I_{EBO}					
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DC Current Gain	$h_{FE(1)}$	V_{CE}	105	0	0	10.5	258.96	324.56	07.02	0	0	7.02	280.98	323.2	13233g2
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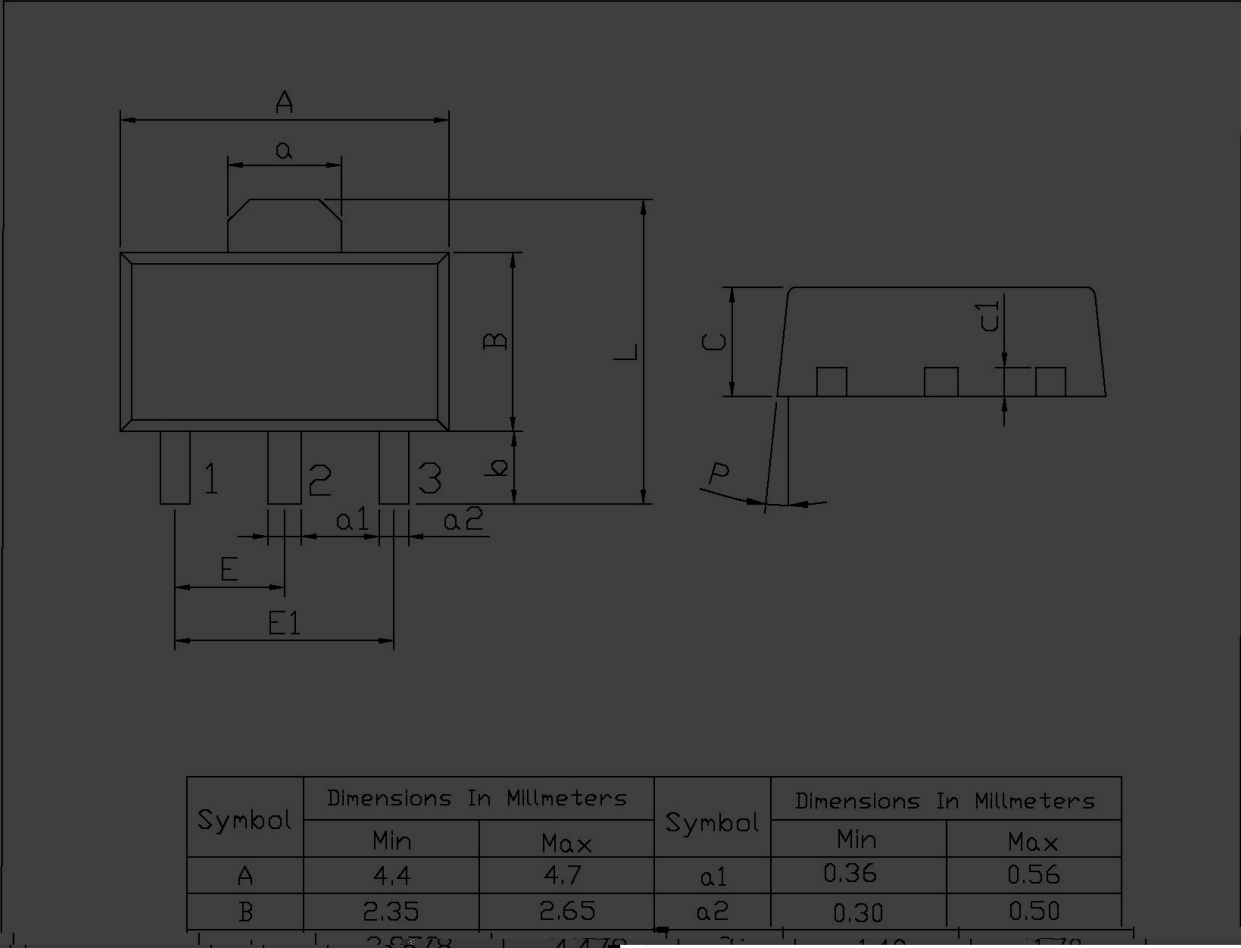
Electrical Characteristic Curve



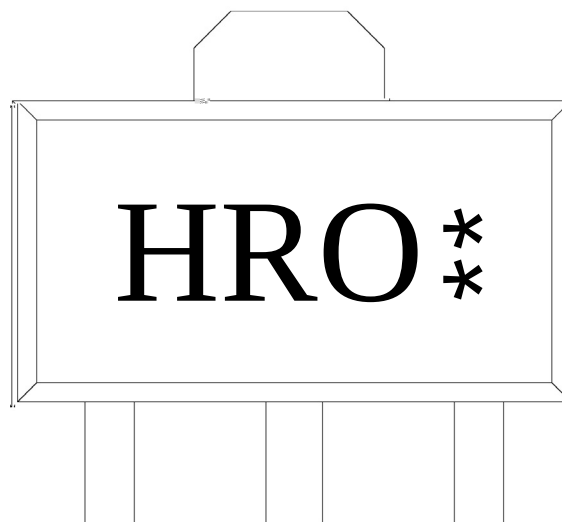
/ Package Dimensions

SOT-89

单位: mm



/ Marking Instructions



H

R

O: h_{FE}

**

Note:

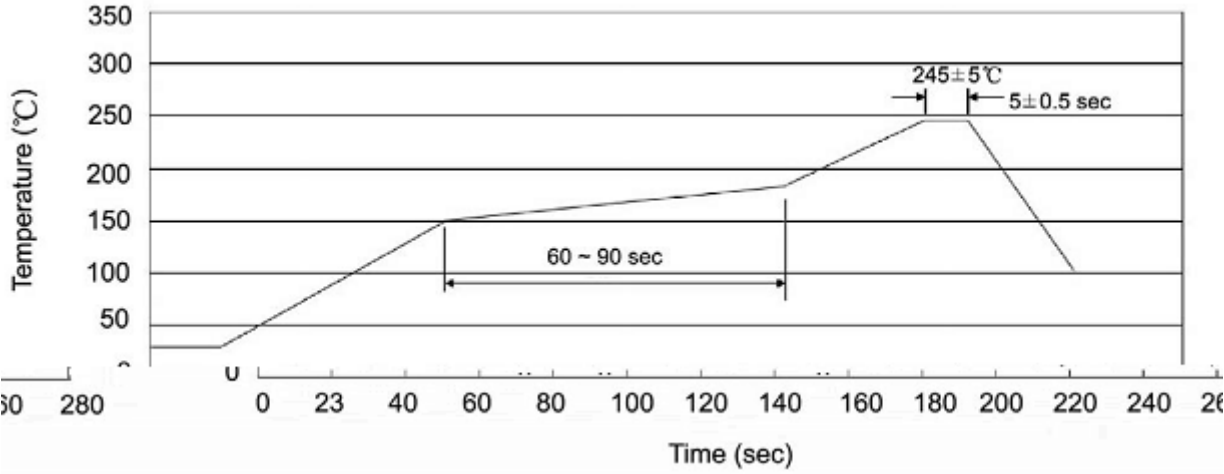
H: Company Code.

R: Product Type.

O: h_{FE} Classifications Symbol

**: Lot No. Code, code change with Lot No.

3. Soldering Temperature Profile



- Note:
- 1. Preheating: 25~150 °C, Time: 60~90sec.
 - 2. Peak Temp.: 245±5 °C, Duration: 5±0.5sec.
 - 3. Cooling Speed: 2~10 °C/sec.

4. Resistance to Soldering Heat Test Conditions

260±5 °C, 10±1 sec. Temp.: 260±5 °C, Time: 10±1 sec

5. Packaging SPEC.

5.1 REEL

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm ³)		
	Units/Reel 只/卷盘	Reels/Inner Box 卷盘/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Reel	Inner Box 盒	Outer Box 箱
SOT-89	1,000	7	7,000	8	56,000	7 ×12	180×120×180	385×257×392

6. Notices